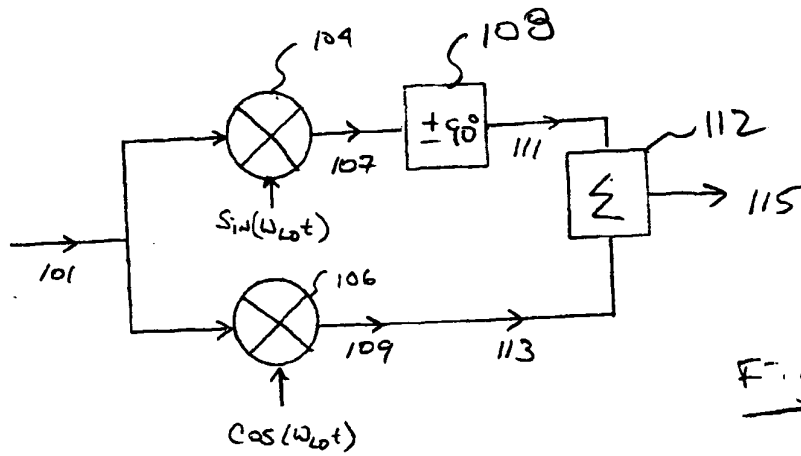


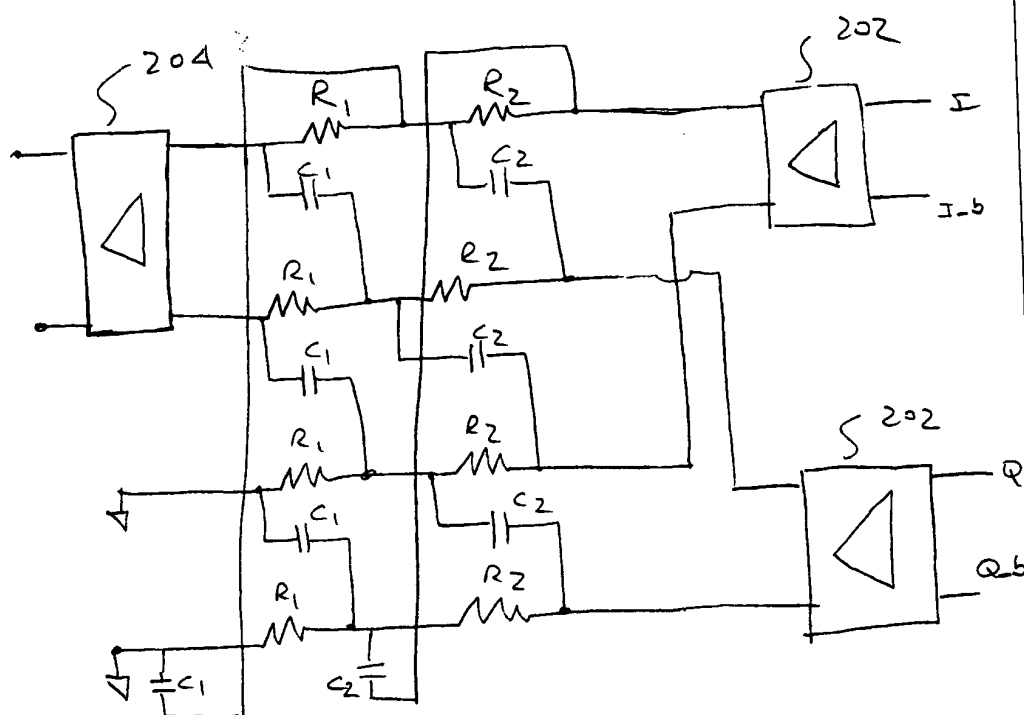
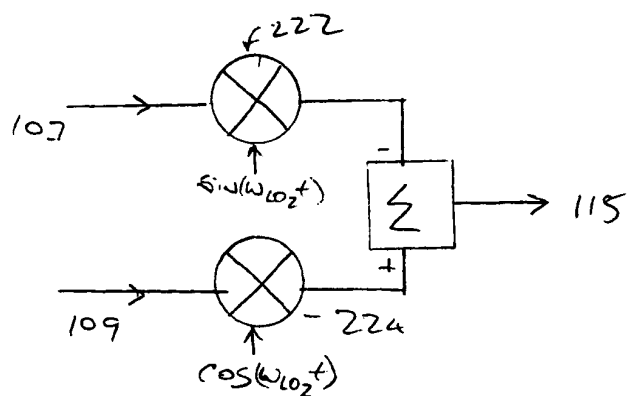
Impedance-Matched IQ Network
for an Image Rejection Circuit

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Fig. 1

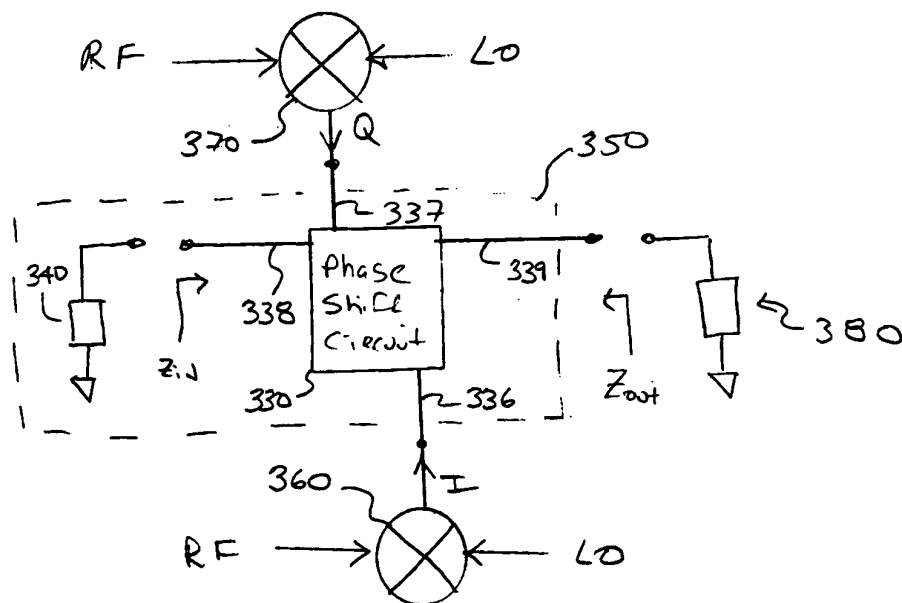
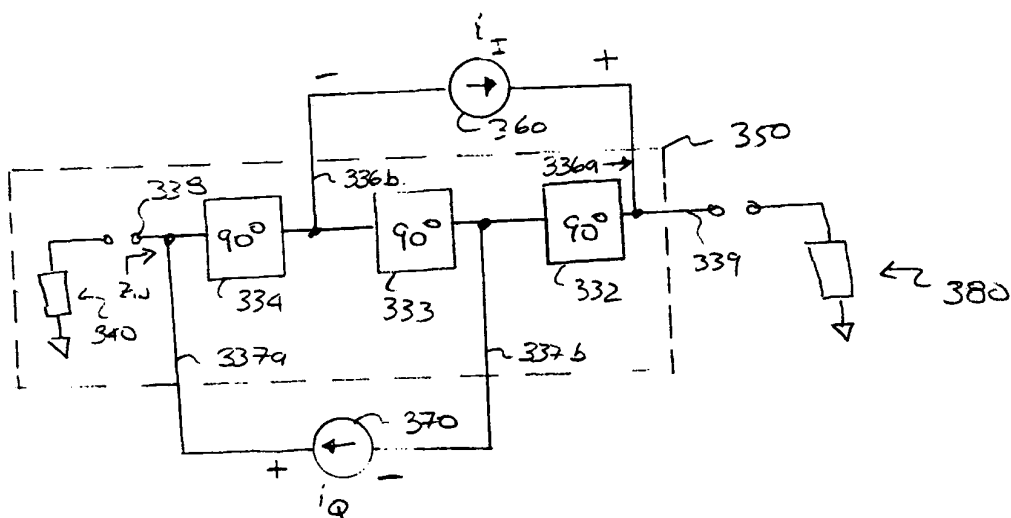
Impedance-Matched IQ Network
for an Image Rejection Circuit

October 8, 2003

Fig. 2AFig. 2B

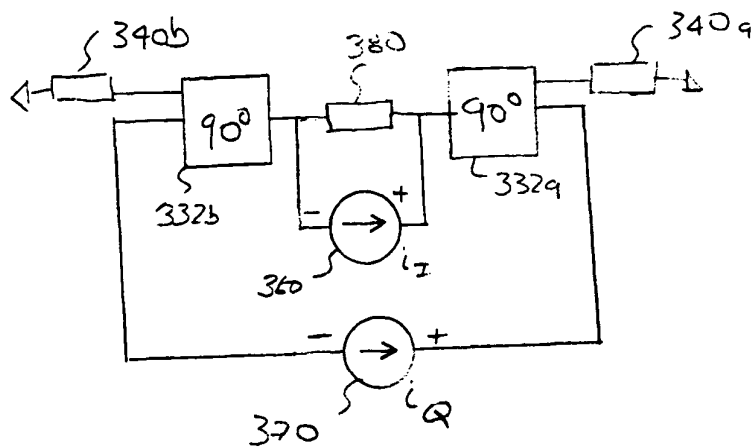
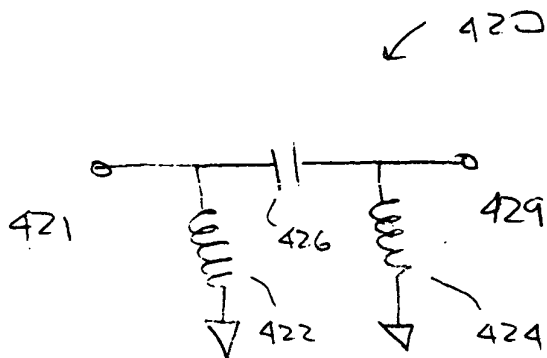
Impedance-Matched IQ Network
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Fig. 3AFig. 3B

Impedance-Matched IQ Network
for an Image Rejection Circuit

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Fig. 3CFig. 4A

Impedance-Matched IQ Network
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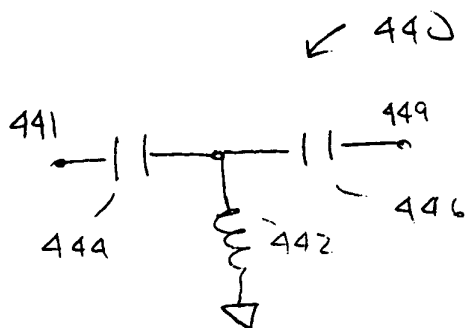


Fig. 4B

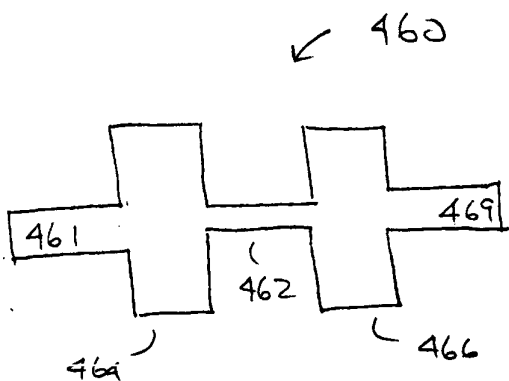


Fig. 4C

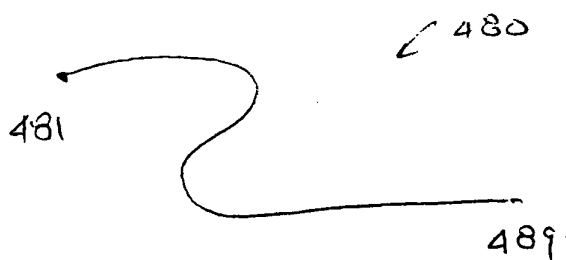


Fig. 4D

Impedance-Matched IQ Network
for an Image Rejection Circuit

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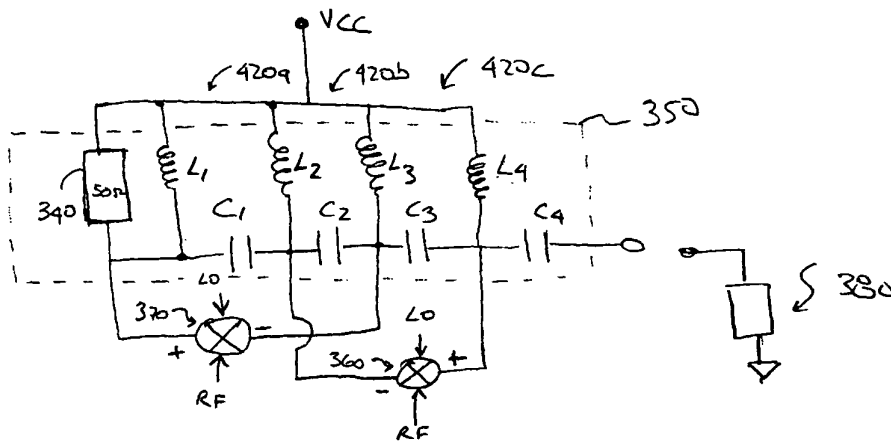


Fig. 5

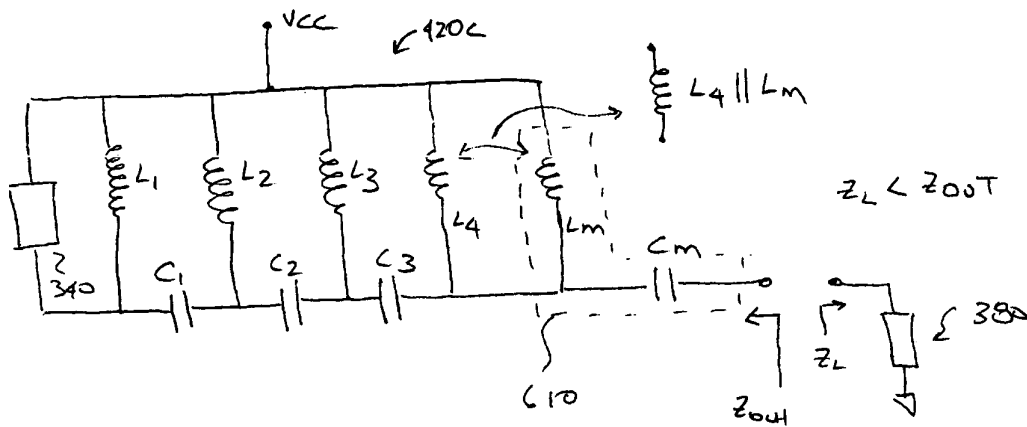


Fig. 6A

Impedance-Matched IQ Network
for an Image Rejection Circuit

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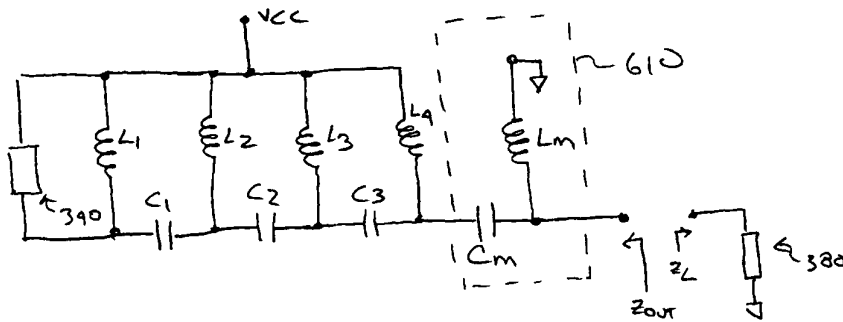


Fig. 6B

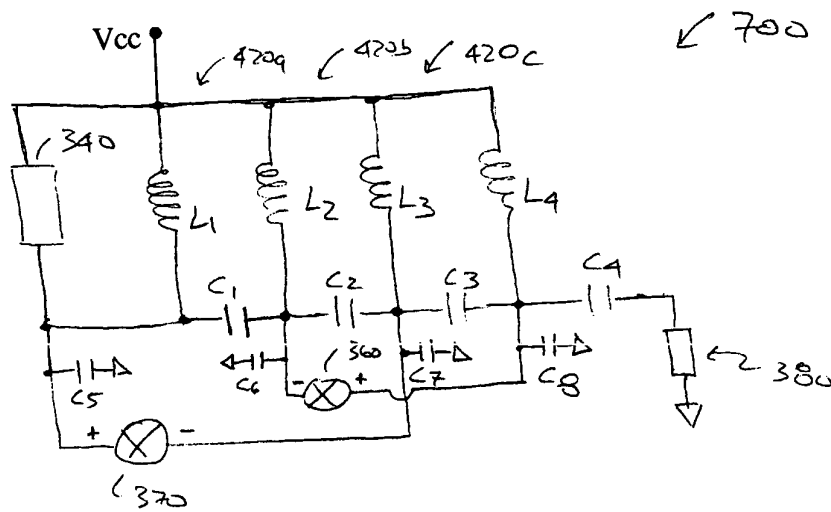


Fig. 7.